

What is claimed is:

1. An input circuit for the detection of an interruption in a differential signal feed, comprising:
5 a pair of input terminals receiving differential data signals; and
two inputs of a data comparator, connected to the input terminals, to generate data, wherein
the two input terminals are connected to respective
10 comparators,
the comparators have an auxiliary voltage applied on the input side, and
signals at outputs of the comparators are evaluated such that an interruption of at least one of the signal
15 feeds is detected.
2. The input circuit as claimed in claim 1, wherein the input circuit is arranged in an integrated circuit.
- 20 3. The input circuit as claimed in claim 1, wherein the input terminals are connected via a resistor to a terminating potential.
4. The input circuit as claimed in claim 3, wherein
25 there is at least one current source which is supplied to the auxiliary voltages at the resistors.
5. The input circuit as claimed in claim 1, wherein the auxiliary voltage is greater than the maximum input
30 offset of the comparators and smaller than a minimum voltage swing of the data signal.
6. The input circuit as claimed in claim 2, wherein resistors are arranged in the integrated circuit.

7. The input circuit as claimed in claim 2, wherein resistors are arranged outside the integrated circuit.

5 8. The input circuit as claimed in claim 7, the input circuit having two current sources, each connected to one of the input terminals and to a supply potential, and each of the two current sources impresses a current which is lower than the currents flowing during normal
10 operation or in a test case.

9. The input circuit as claimed in claim 1, wherein the outputs of the comparators are respectively connected to a boundary scan cell of a boundary scan shift register.
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10. The input circuit as claimed in claim 1, wherein the input circuit is configured to be switched off.

11. A method for detection of an interruption in a differential signal feed, comprising:
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feeding differential data signals to a pair of input terminals; and

generating data at the pair of input terminals which are connected to two inputs of a data comparator, wherein

25 the pair of input terminals are connected to a comparator,

the comparators have an auxiliary voltage applied on the input side, and

30 the signals at the outputs of the comparators are evaluated such that an interruption of at least one of the signal feeds is detected.